

## **Biography of Prof. Jaeseok Kim**



**Prof. Jaeseok Kim, Ph.D.**, received the B.S. degree from Yonsei University, Seoul, Korea in 1977, M.S. degree from KAIST, Korea in 1979, and Ph. D degree from Rensselaer Polytech Institute, NY, USA in 1988. From 1988 to 1993, he was a member of technical staff at the AT&T Bell Lab., Murray Hill, NJ, USA. And, he was a director of ETRI which is Korea government research institute from 1993 to 1995. Since 1996, He is a professor of school of electrical and electronic engineering and director of IT SoC(System-On-Chip) research center at Yonsei University, Seoul, Korea.

He served as Dean of electrical & mechanical division and also served as director of Information System Office at Yonsei university. He was a distinguished lecturer of IEEE Circuit & System society from 2011 to 2015. He also served as general director of System IC R&B Korea government project (total research budget of 120 M\$) from 2012 to 2016.

He received an excellence of quality award from AT&T Bell Lab. in 1993 and the best brain award in Korea semiconductor technology field in 2005. He was honored as the best research achievement professor of year 2006, 2008, and 2015 at Yonsei University. He also received the best contribution award in patents from Korea Intellectual Property Office in 2014. In 2018, he received Haedong award (\$25,000 in US) which was established by KIEE(Korea Inst. of Electrical & Electronics Eng.) to honor the efforts of those who have accomplished great achievements in the field of electronic engineering in Korea.

He has published 72 international (SCI) journal papers, 125 international conference papers, 204 domestic journal/conference papers with 9 technical text books in electrical engineering area. He holds 17 international patents and 103 Korea patents. He has transferred 7 patents/IPs to 13 companies with total initial payment of 500K \$.

He has led 29 doctoral students and 62 master students as primary advisor. He is now leading the IT SoC research lab. with 6 doctoral students and 1 master student. His research interests include wireless communication/multimedia SoC design, ADAS signal processing, high performance image processing, and IoT platform architecture.